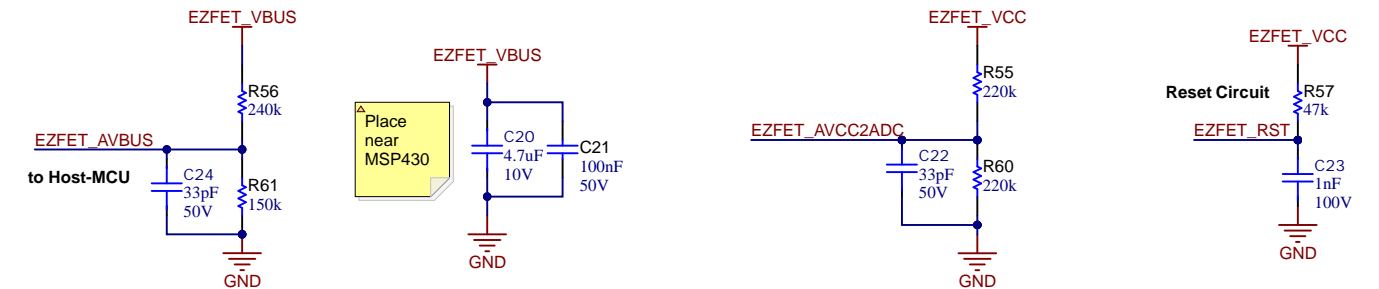
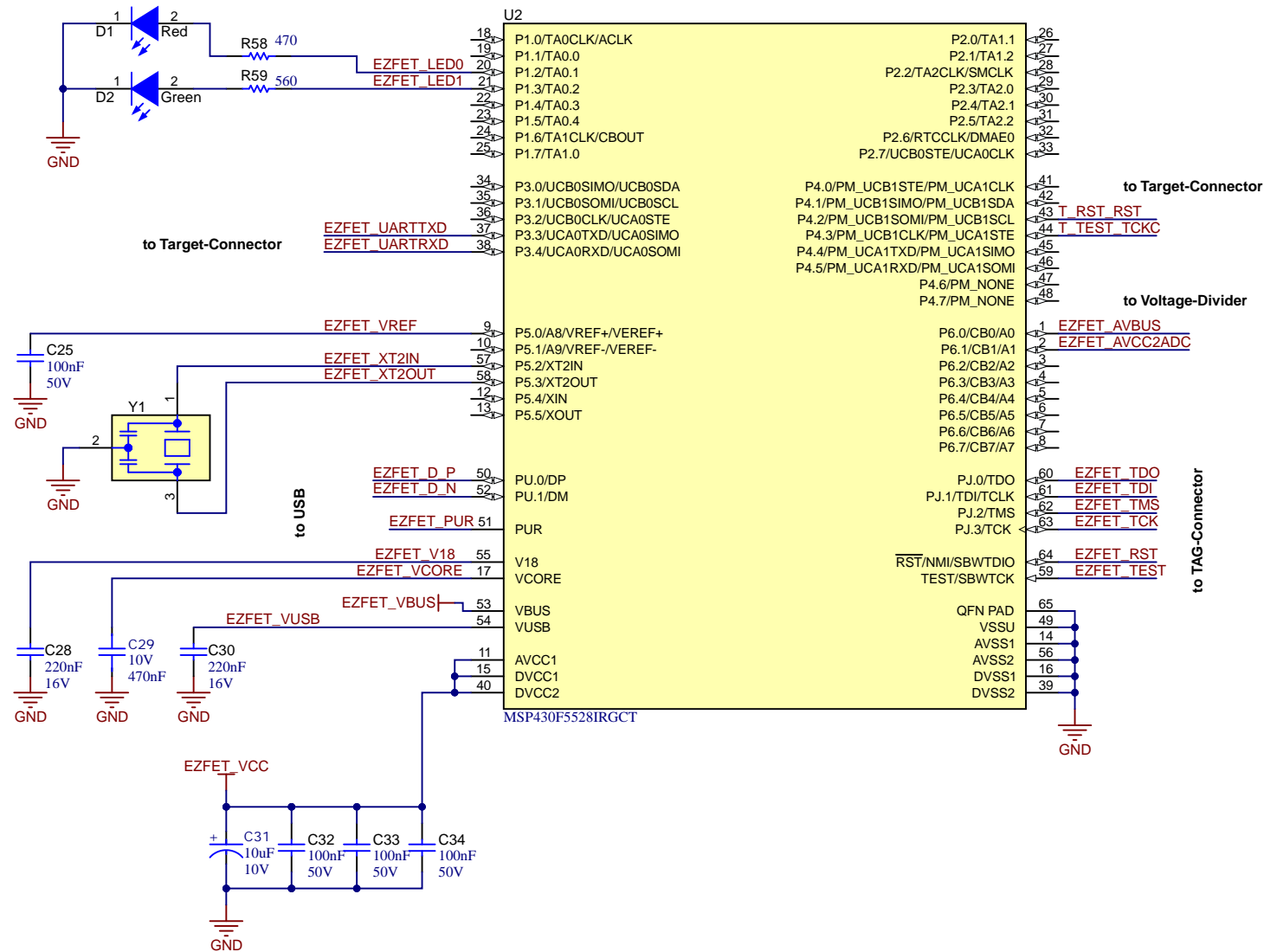
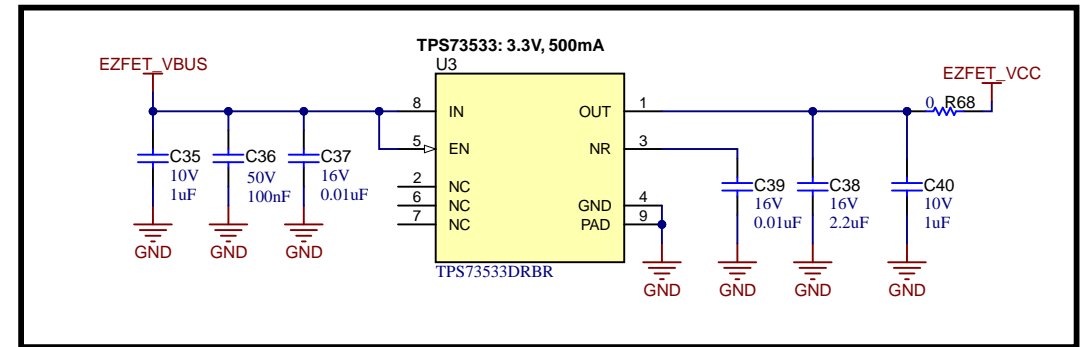


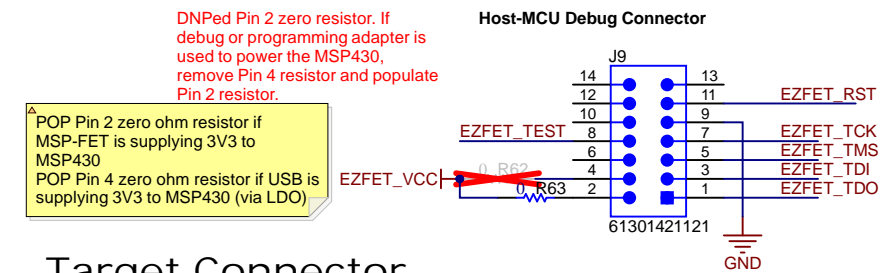
Host MCU for Emulation



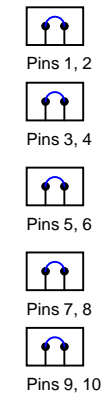
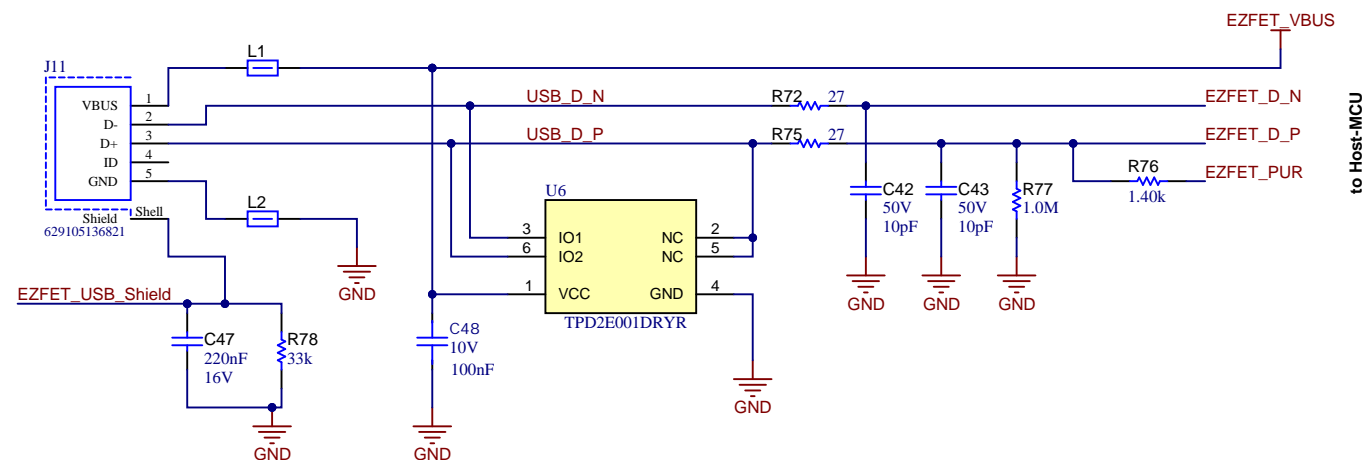
3.3V Power (EZFET_VCC)



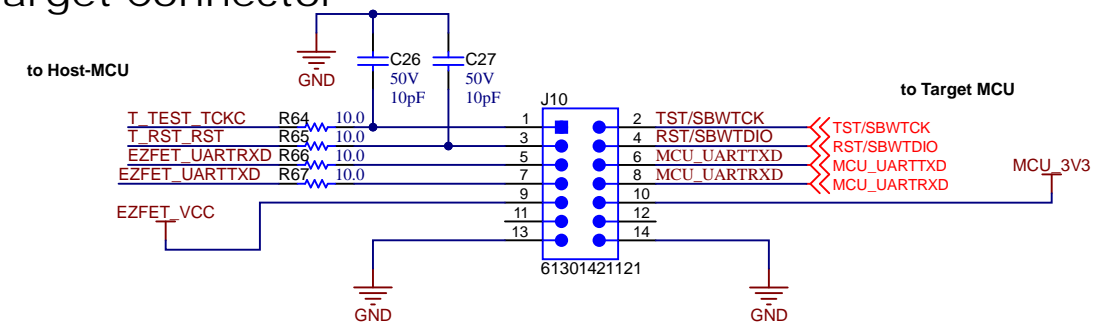
JTAG-Connector (Host Debug)




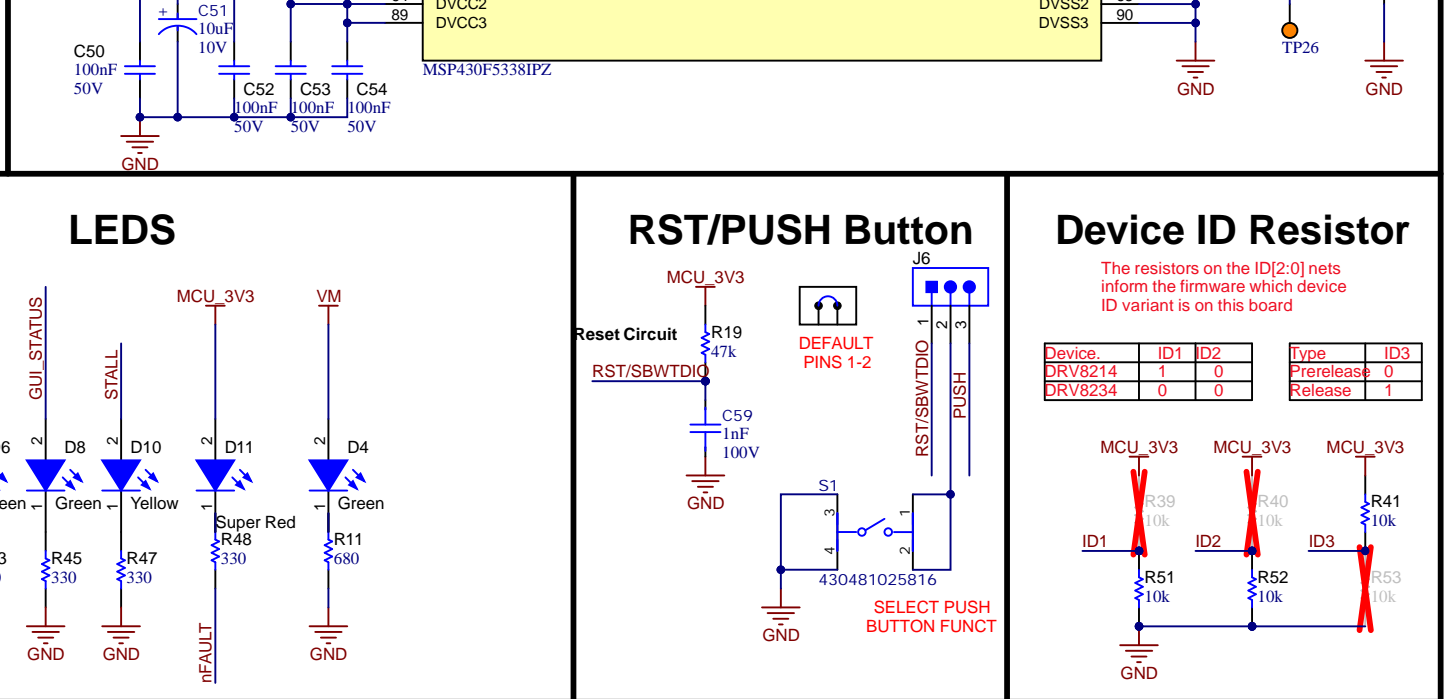
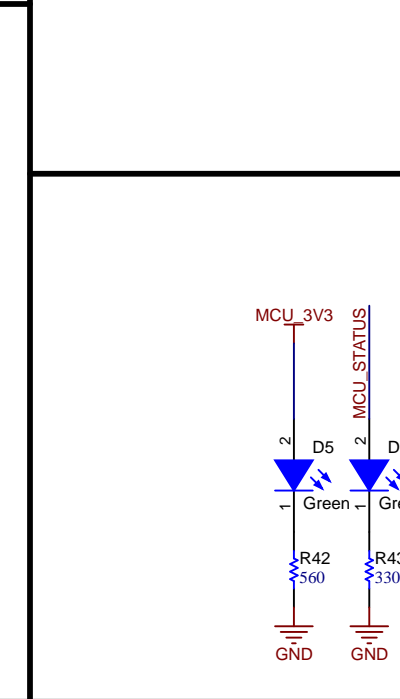
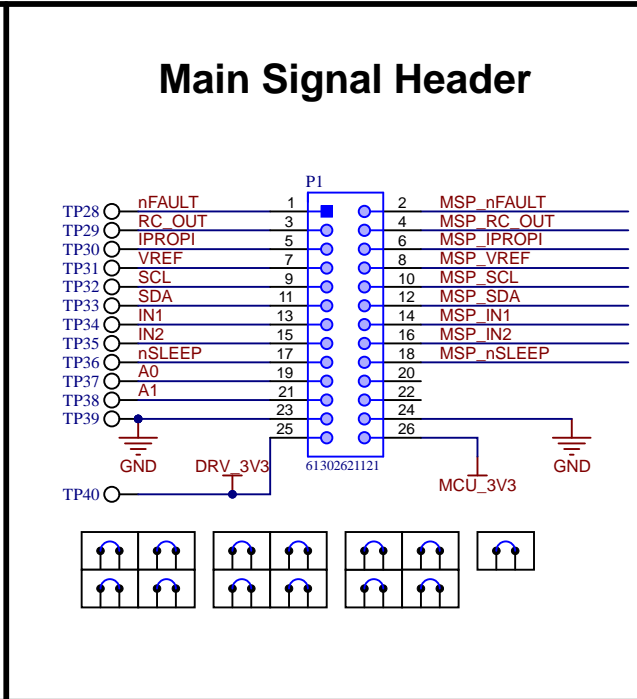
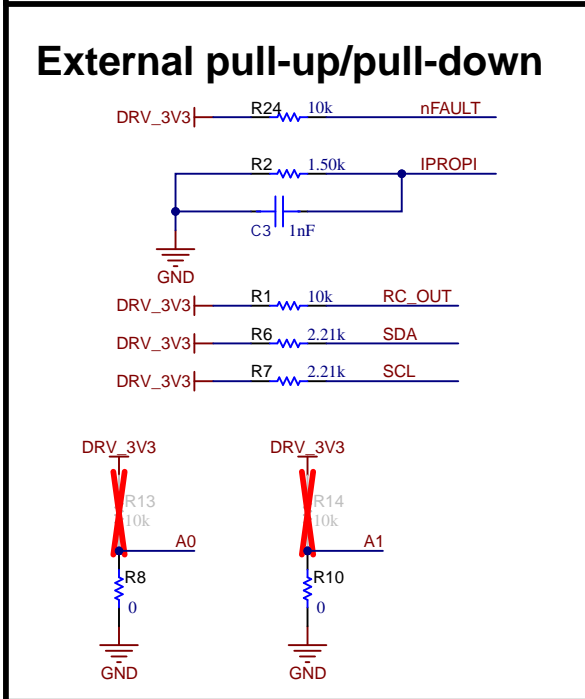
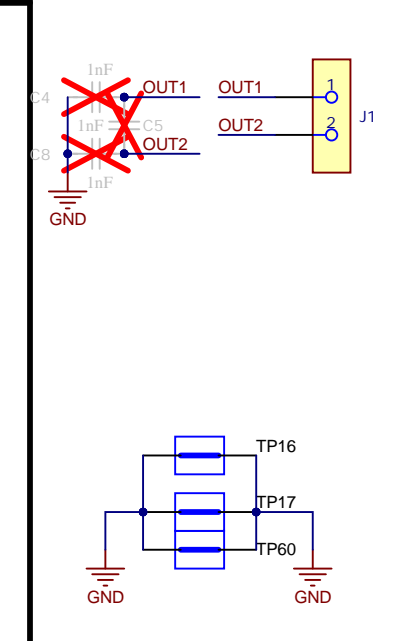
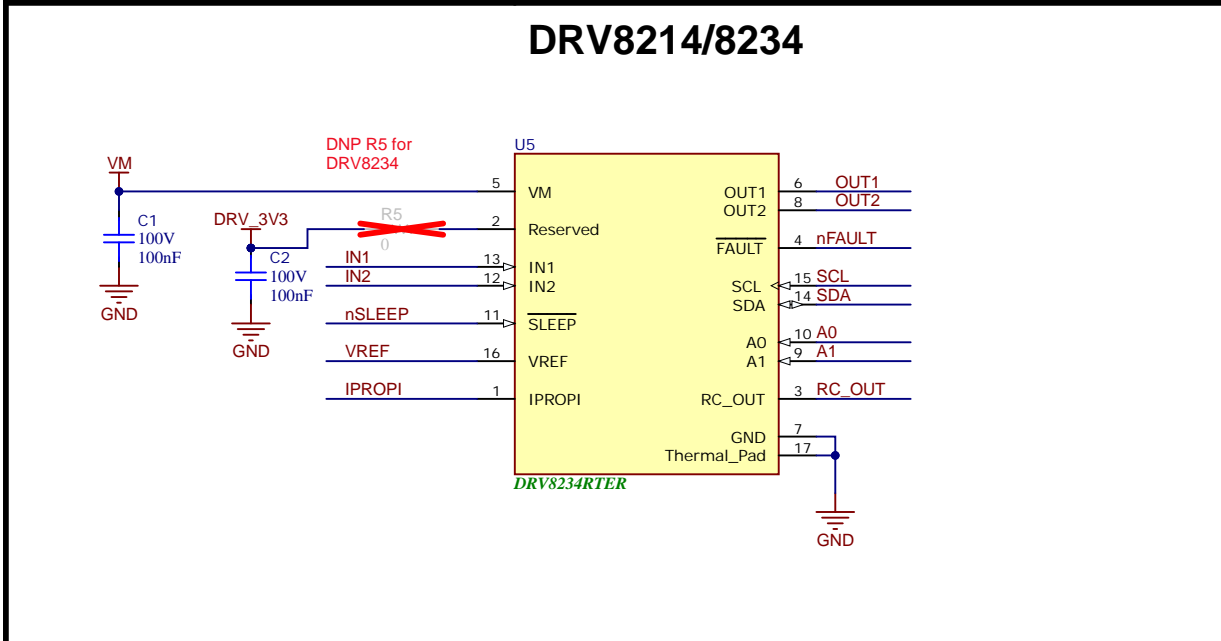
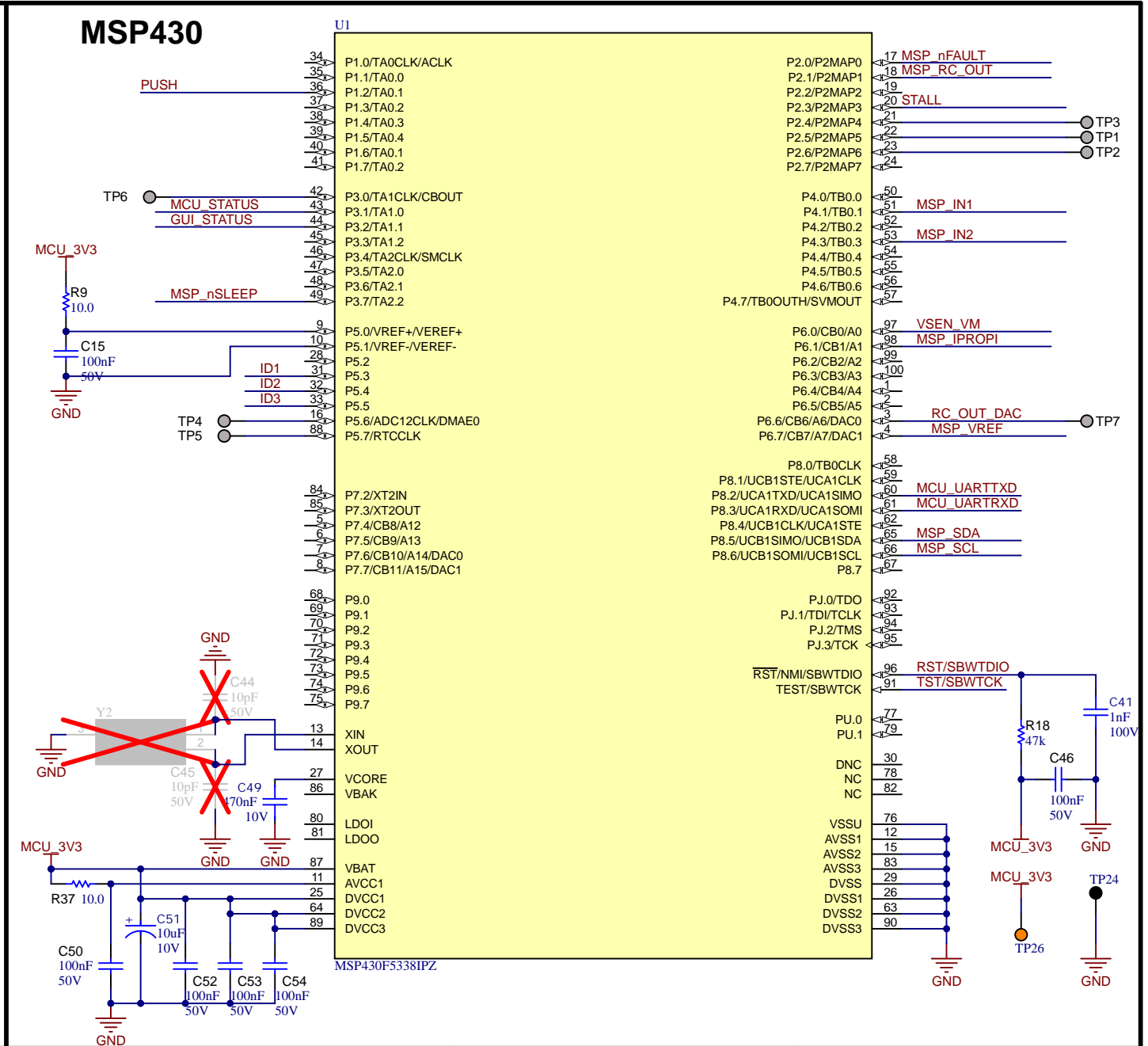
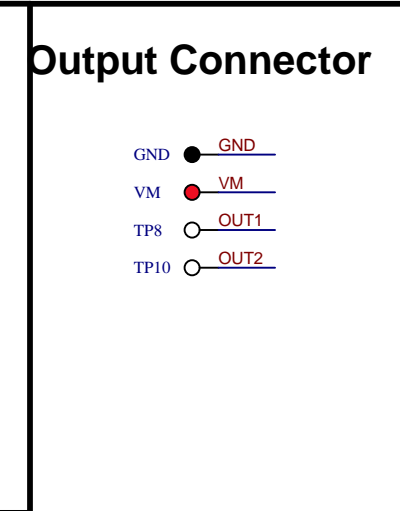
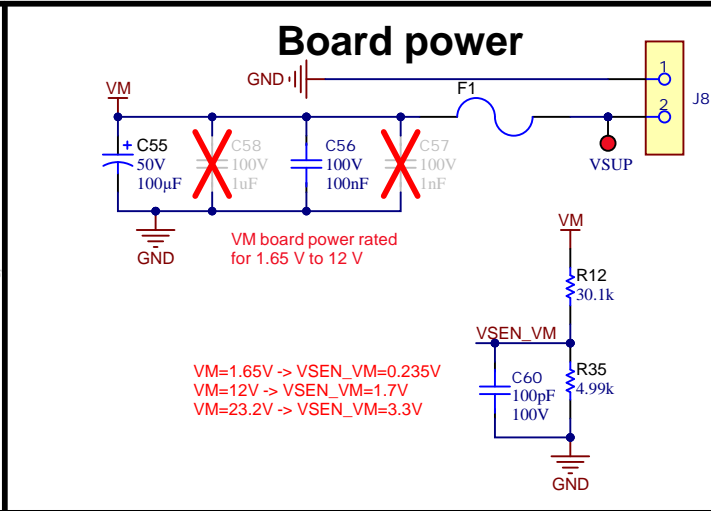
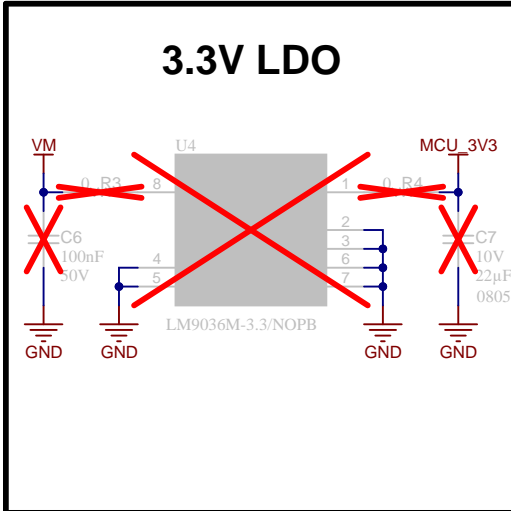
USB-Interface



Target Connector

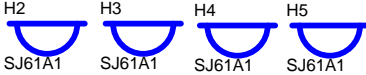


Orderable: DRV8234EVM	Designed for: Public Release	Mod. Date: 10/25/2023	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2023
TID #: N/A	Project Title: DRV8414_8434EVM		
Number: MD069	Rev: B	Sheet Title:	
SVN Rev: Unknown revision	Assembly Variant: 002	Sheet: 1 of 1	
Drawn By: Pablo Armet	File: MD069B_ezFET.SchDoc	Size: B	
Engineer: Jacob Thompson	Contact: http://www.ti.com/support		





PCB Number: MD069
PCB Rev: B



LBL1
PCB Label
THT-14-423-10
Size: 0.65" x 0.20 "

Label Table	
Variant	Label Text
001	DRV8214EVM
002	DRV8234EVM

ZZ1
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ4
Label Assembly Note
This Assembly Note is for PCB labels only

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